F19.1

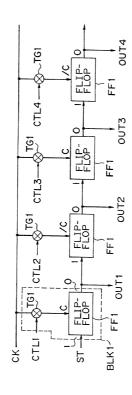
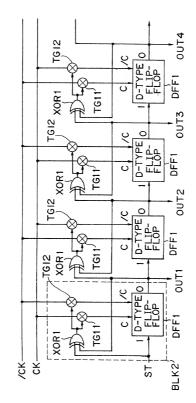
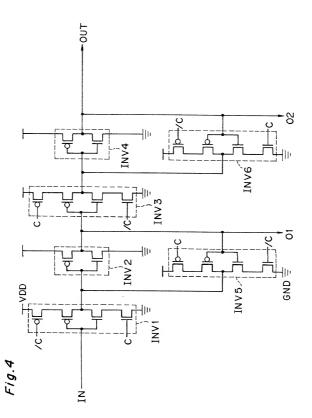
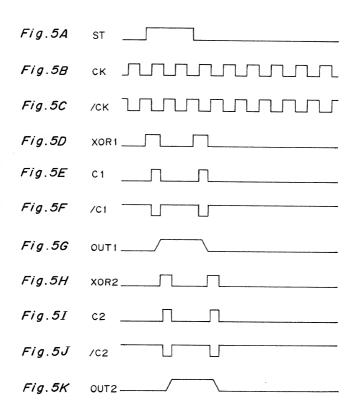


Fig.2A	ST
Fig.2B	ск ЛЛЛЛЛЛЛЛЛ
Fig.2C	CTL1
Fig.2D	OUT1
Fig.2E	CTL2
Fig.2F	OUT2
Fig.2G	CTL3
Fig.2H	OUT3
Fig.2I	CTL4
Fig.2J	OUT4

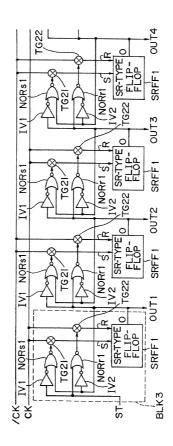
Fig.3

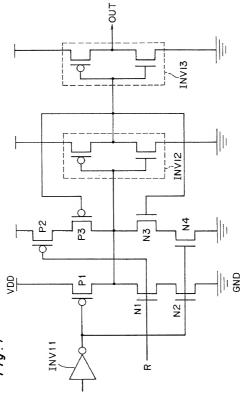






F19.6





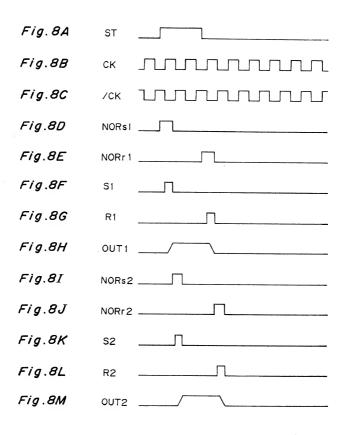
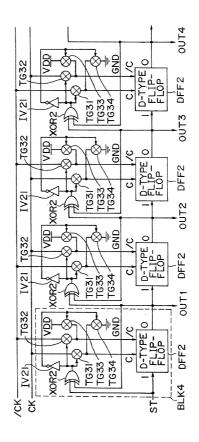
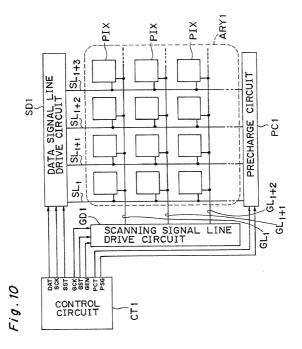
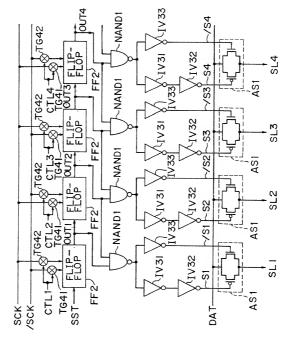


Fig.9









F19.12

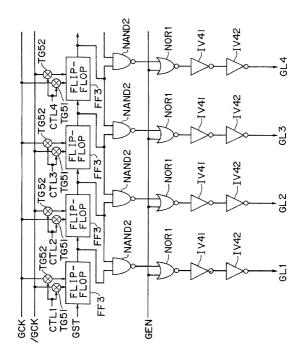


Fig. 13A	SST
Fig./38	SCK
Fig.13C	CTL1
Fig.13D	OUT1
Fig.13E	CTL2
Fig.I3F	OUT2
Fig./3G	CTL3
Fig.I3H	OUT3
Fig.13I	CTL4
Fig.13J	OUT4

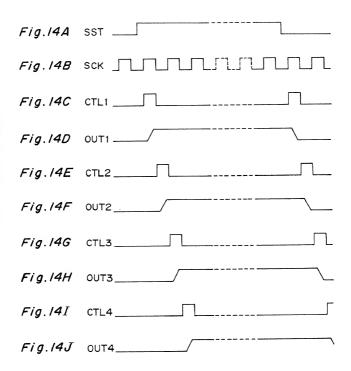
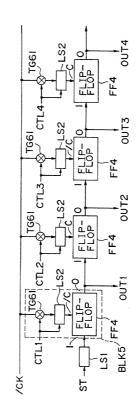


Fig. 15



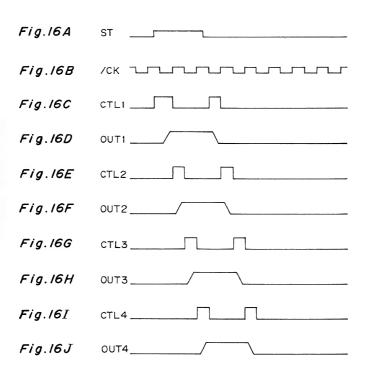
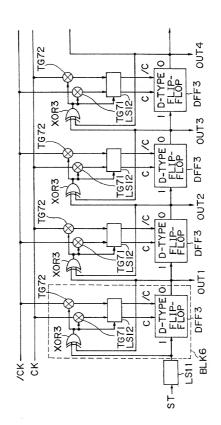


Fig.17



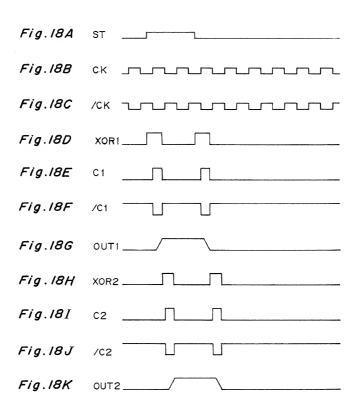
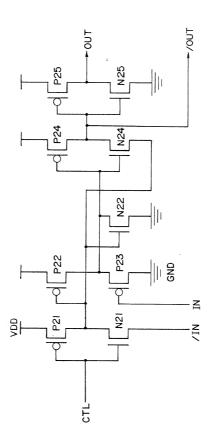
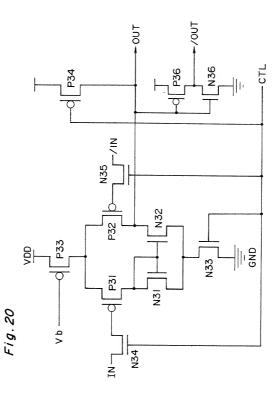


Fig.19





F19.21

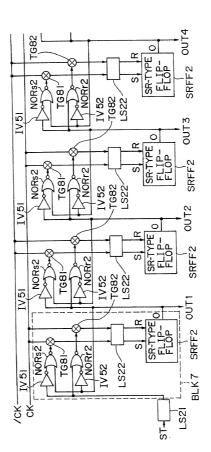


Fig. 22 A	ST
Fig.22B	
Fig.22C	/CK 127272727272727272727272727272727272727
Fig.22D	NORs1
Fig.22E	NORr1
Fig.22F	S1
Fig.22G	R1
Fig.22H	OUT1
Fig.22I	NORs2
Fig.22J	NORr2
Fig. 22K	S2
Fig. 22L	R2
Fia.22M	OUT2

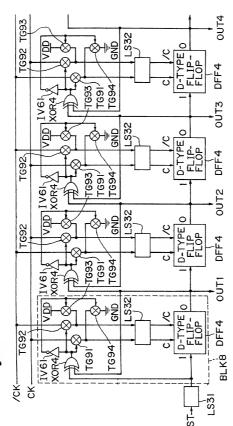
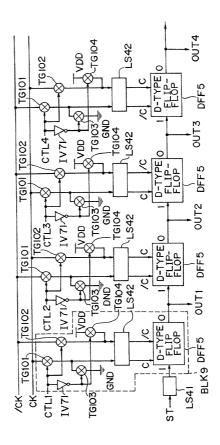


Fig.23

Fig. 24



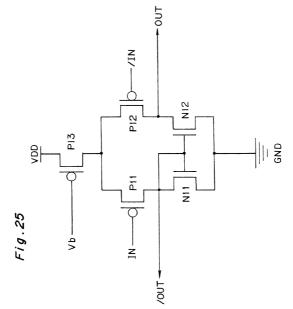
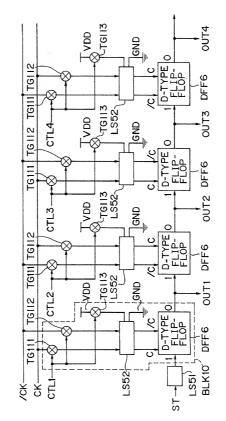


Fig. 26



F19.27

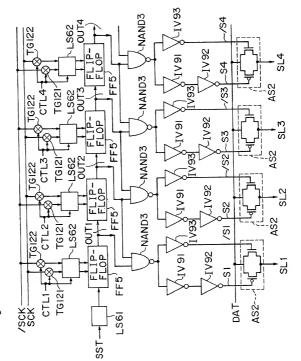


Fig.28

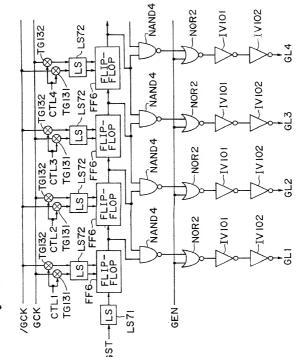


Fig.29A	SST
Fig.29B	SCK JULIANIA
Fig.29C	CTL1
Fig.29D	OUT1
Fig.29E	CTL2
Fig.29F	OUT2
Fig.29G	CTL3
Fig.29H	OUT3
Fig.291	CTL4
Fia.29J	OUT4

Fig. 30A	SST	
Fig.30B	SCK	
Fig.30C	CTL1	
Fig.30D	OUT1	
Fig.30E	CTL2	
Fig.30F	OUT2	
Fig.30G	CTL3	
Fig.30H	OUT3	
Fig.301	CTL4	
Fia.30J	OUT 4	



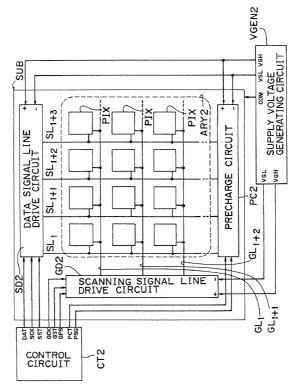
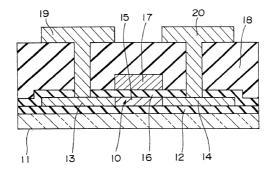
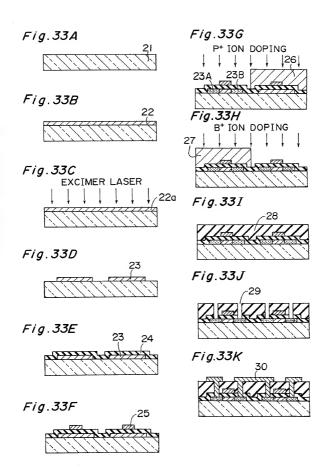


Fig.32





SL1+3 DATA SIGNAL LINE DRIVE CIRCUIT PRECHARGE CIRCUIT S-1+2 SD3 PC3 SL1+1 ار ا Fig. 34 PRIOR ART 603 GĽ 1+1 SCANNING SIGNAL LINE DRIVE CIRCUIT GL1 GCK GEN GEN PCT SCK CONTROL CIRCUIT CT3

PIX

**ÀRY3** 

PIX

PIX

Fig. 35 PRIOR ART

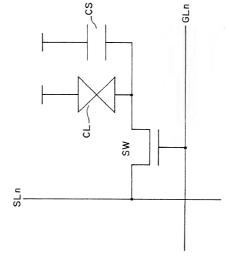


FIG. 36 PRIOR ART

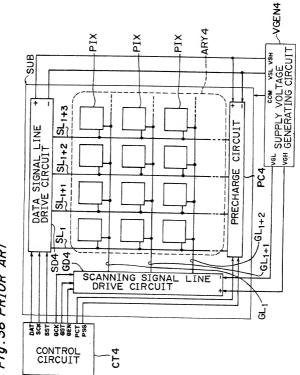


Fig. 37 PRIOR ART

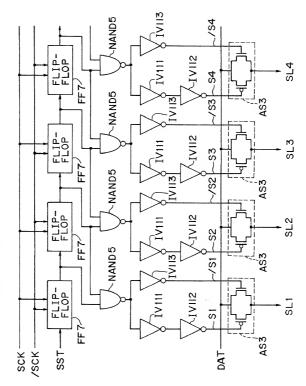


FIG.38 PRIOR ART

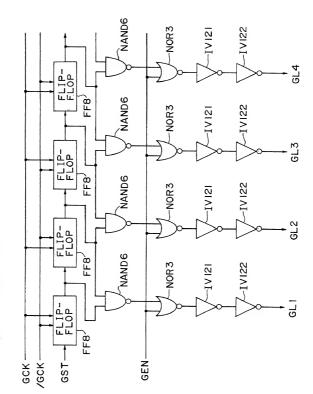


FIG. 39 PRIOR ART

